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APPLICANTS

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** CONTINUING DATA *****

This application is a 371 of PCT/IB03/03429 08/04/2003 *MV*

** FOREIGN APPLICATIONS *****

EUROPEAN PATENT OFFICE (EPO) 02078785.9 09/13/2002 *MV*

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY NETHERLANDS	SHEETS DRAWING 3	TOTAL CLAIMS <i>1</i>	INDEPENDENT CLAIMS <i>2</i>
35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verifier and Acknowledged <i>MV</i> Examiner's Signature <i>MV</i> Initials <i>MV</i>				

ADDRESS

24738

TITLE

Reduced chip testing scheme at wafer level

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